

### REAL TIME CLOCK MODULE (SPI-Bus)

Time stamp function and Low current consumption

## **RX4111CE**

· Built in frequency adjusted 32.768 kHz crystal unit Interface Type SPI -Bus 4 wire Low backup current : 100 nA Typ. / 3 V

· Auto power switching function : Automatically switches to backup power

supply by monitoring the VDD voltage.

 Time stamp function : 8 times stamped from year to 1/256 seconds Interrupt output : Wake up every minute or every second Alarm interruption : Day, date, hour, minute, second

· Auto repeat wakeup timer interruption

 Self-monitoring interruption : Crystal oscillation stop, V<sub>BAT</sub> low, V<sub>DD</sub> low



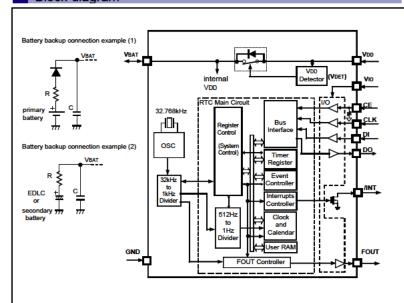
Product Number (2,000 pcs / Reel) RX4111CE A: X1B000431000115 RX4111CE B: X1B000431000215



#### **RX4111CE**

 $(3.2 \times 2.5 \text{ mm}, t = 1.0 \text{ mm Max.})$ 

## Block diagram



#### Overview

- Interface type
- SPI-Bus interface (4 wire, 4 MHz Max.)
- Auto power switch function

The V<sub>DD</sub> voltage is monitored and it switches to the backup power supply by the automatic operation Backup power supply switching voltage 1.2V Min.

Clock output function

Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz When the clock output is not used, the FOUT pin can be used as a timer output pin (CMOS)

Wakeup timer function

Selectable from 244 µs to 32 years (24 bit x 1 ch.) Timer source clock selectable from 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz Auto release after interrupt output from /INT pin at timer completes

This operation is auto repeat with a selected cycle, it can be used like a watchdog timer

Time stamp function

8 times stamped from year to 1/256 seconds The time stamp trigger inputs from self-monitoring and SPI command

Alarm function

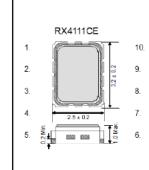
It is possible program from year to second

 Self-monitoring interruption Crystal oscillation stop, VBAT low, VDD low

#### Pin Functin

| Signal Name | 1/0    | Function   |
|-------------|--------|--|
| CE          | Input  | Chip enables input pin   |
| CLK         | Input  | Serial clock input pin   |
| DI          | Input  | Serial data input pin  |
| DO          | Output | Serial data output pin   |
| FOUT        | Output | Frequency output (CMOS)<br>(frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)  |
| / NT        | Output | Interrupts output by Alarm and Timer events (N-ch. open drain)   |
| VDD         | -      | Power supply pin Possible to supply different voltage from VIO   |
| VIO         | -      | Interface power supply pin Input to supply the voltage same as a host  |
| VBAT        | -      | Power supply pin for backup battery Connect an EDLC, a secondary battery, a primary battery In the backup voltage range, supplied to IC, from this pin |
| GND         | -      | Ground pin   |

#### Terminal connection / External dimensions (Unit: mm)



| Pin | Connection |  |  |  |  |
|-----|------------|--|--|--|--|
| 1   | Vdd        |  |  |  |  |
| 2   | VBAT       |  |  |  |  |
| 3   | DI         |  |  |  |  |
| 4   | FOUT       |  |  |  |  |
| 5   | CLK        |  |  |  |  |
| 6   | DO         |  |  |  |  |
| 7   | CE         |  |  |  |  |
| 8   | Vio        |  |  |  |  |
| 9   | GND        |  |  |  |  |
| 10  | /INT       |  |  |  |  |

#### Specifications (characteristics)

#### ■ Recommended Operating Conditions Max. Unit Тур. Operating supply voltage 1.6 30 55 ٧ Clock supply voltage VCLK 30 55 1.1 Operating temperature Ta -40 +25 +85 °С

-VDET1

Vpp detect voltage ■ Frequency characteristics

| = 1 requeries characteristics |      |                          |                            |       |      |       |        |  |
|-------------------------------|------|--------------------------|----------------------------|-------|------|-------|--------|--|
| tem Grade                     |      | Symbol                   | Conditions                 | Min.  | Тур. | Max.  | Unit   |  |
| Fragueney telerance           | Α    | Δf/f                     | Ta = +25 °C<br>VDD = 3.0 V | -11 5 | 1    | +11 5 | x 10⁻⁵ |  |
| Frequency tolerance           | В    |                          |                            | -23   | -    | +23   |        |  |
| Oscillation start-up t        | tsta | VDD = 2.75 V<br>to 5 5 V | 1                          | 03    | 10   | s     |        |  |

VDD, Fall

1.40

1.20

#### Refer to application manual for details

| ■ Current consumption characteristics |        |   | Ta = -40 °C to +85 °C |      |      |      |
|---------------------------------------|--------|---|-----------------------|------|------|------|
| tem                                   | Symbol | Conditions  | Min.                  | Тур. | Max. | Unit |
| Current consumption                   | Іват   | Input pins = "L",<br>FOUT = OFF, INT = OFF,<br>VBAT = 3 0 V, VDD = VIO = 0 0 V,<br>CHGEN = 0b, N EN = 0b,<br>SWSEL0 = 1, SWSEL1 = 0 | 1                     | 100  | 450  | nΑ   |
|                                       | l32k   | Input pins = "L",<br>FOUT = 32.768 kHz, / NT = OFF,<br>VDD = VIo = 3.0 V,<br>FOUT pin CL = 15 pF,<br>CHGEN = 0b, N EN = 1b          | ı                     | 2.0  | 30   | μА   |

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